



NONLINEAR DYNAMICS OF BUCK CONVERTER

Dmitry Pikulin

Riga Technical University, Faculty of Electronics and Telecommunications

Azenes 12, Riga, Latvia

Ph.: +(371)26565214, e-mail: pikulin03@inbox.lv

Abstract. *This paper is concerned with the problem of modeling and simulation of piecewise linear systems with the pulse width modulation control. Models of this kind of systems are widely used in engineering practice, in particular in power electronic converters. The research provides the study of chaos and bifurcations in one of the switching converters – step-down converter under voltage mode control, operating in discontinuous and continuous current modes, using Matlab and Simulink simulation environment. Various types of models, including discrete – time maps, switched state – space models are examined in order to detect their suitability and reliability in numerical investigation of nonlinear phenomena in DC–DC converters. Some analytical methods for the search of periodic regimes and their stability estimation were also used in order to validate numerically obtained results and evaluate the accuracy of models used. As direct simulation does not accurately pinpoint bifurcation points and computes only stable invariant sets, some aspects and advantages of numerical path-following are also discussed.*

Keywords: *bifurcation diagrams, buck converter, chaos, path – following, simulation tools.*

Introduction

High efficiency solid state power conversion has become possible through the continuing development of high power semiconductor devices. The operation of these devices as switches, which is necessary for high efficiency, means that power electronic circuits are essentially nonlinear, time varying dynamical systems. Though this makes them difficult to study, the effort is well worthwhile as these systems are becoming increasingly important in the delivery of electrical energy.

In the case of switch mode power converters (SMPC) the most commonly used models are: averaged model (AM), switched state-space model (SSSM) and iterative mapping (IM). A considerable amount of work in analysis of nonlinear phenomena in DC-DC power converters has been reported during the latest three decades [1-4]. It has been proved that the use of AM, commonly accepted in engineering practice, does not allow determination of the existence of the majority of nonlinear phenomena in SMPC. Moreover, Tse in his study [5] showed that the conventional analysis using averaging is qualitatively wrong: it predicts stability in large parameter range, whereas in reality higher order orbits and chaos are present. Therefore, rather than recurring to AM, which are standard tools for the analysis of this kind of circuits, in our investigation we provide the analysis of the bifurcation phenomena in SMPS, highlighting regions where subharmonic and even aperiodic behavior is possible, using the most suitable nonlinear models (SSSM and IM).

Materials and methods

Step-down (buck) switching converter is one of the most popular circuits in modern electronics. It is especially widely used in the field of handheld devices that require many different voltage levels to be supplied by the same battery with minimal waste of energy. Among many possible implementations of this converter we focus on the pulse-width modulated (PWM) buck DC-DC converter with voltage mode control, whose circuit is schematized in Fig.1.

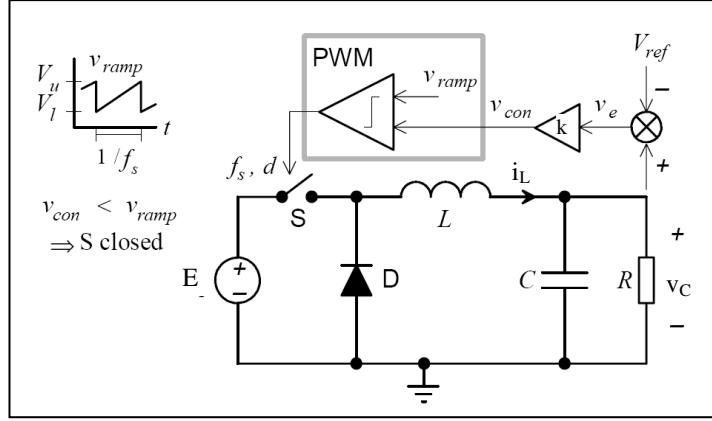


Fig.1. PWM buck converter under voltage mode control

The aim of buck converter is to preserve preferred output voltage lower than that provided by the input source. This can be achieved by appropriately turning on and off the switch, so that the circuit is repeatedly forced by the external voltage source. The SSSM consists of three linear subsystems depending on the value of inductor current and switching condition – the value of the output voltage is compared with the ramp signal to generate the switching signal.

$$\begin{cases} \frac{dv_C}{dt} = \frac{1}{C} \left(i_L - \frac{v_C}{R} \right) & \text{for } v_C \geq \frac{V_{ramp}}{k} + V_{ref}; \\ \frac{di_L}{dt} = \frac{1}{L} (-v_C) \end{cases} \quad (1)$$

$$\begin{cases} \frac{dv_C}{dt} = \frac{1}{C} \left(i_L - \frac{v_C}{R} \right) & \text{for } v_C < \frac{V_{ramp}}{k} + V_{ref} \text{ and } i_L > 0; \\ \frac{di_L}{dt} = \frac{1}{L} (E - v_C) \end{cases} \quad (2)$$

$$\begin{cases} \frac{dv_C}{dt} = \frac{1}{C} \left(-\frac{v_C}{R} \right) & \text{for } v_C < \frac{V_{ramp}}{k} + V_{ref} \text{ and } i_L = 0; \\ \frac{di_L}{dt} = 0 \end{cases} \quad (3)$$

$$V_{ramp} = V_L + (V_U - V_L) \left(\frac{t}{T} \bmod 1 \right) \quad (4)$$

where v_C corresponds to output voltage; i_L – inductor current; k is the gain of the error amplifier; V_{ref} – reference voltage; V_{ramp} – is an external ramp signal with period T and upper and lower threshold voltages V_U and V_L , respectively, that is used to determine the switching instants; C, E, L and R are positive constants representing the capacitance, input source voltage, inductance and resistance, respectively.

Typically in high-power applications, where current ripple must be small to minimize losses, the components and threshold signal are chosen so that the inductor current is always strictly positive – the circuit is said to work in continuous conduction mode (CCM) and SSSM includes equations (1), (2), (4). On the other hand, for low-power applications, where opening and closing of switches causes a significant amount of overall losses, lower switching frequencies are used, letting the inductor current go to zero – the converter is operating in discontinuous conduction mode (DCM) and SSSM includes equations (1)-(4).

The best way to study the dynamics of the buck converter is to obtain a summary chart of the different types of behavior exhibited by circuit when some parameters are varied. For piecewise smooth systems there are two classes of numerical methods for investigating the possible dynamics for the range of parameters, namely: *brute force approach* and *numerical continuation* (also known as *path-following*) [6].

In order to determine stable behavior, direct numerical simulation methods can be used in an event-driven manner (brute-force approach). That is, for a given initial state, the equation (1) is solved exactly or via high – order time-stepping scheme such as Runge-Kutta method. The test function is monitored in order to determine the next switching point. After applying the switching law, this point is then used as the starting point for the next calculation (solving equation (2) or (3)). This process could be easily applied to calculate flows with many switching events or points of IM. To determine possible sets for particular parameter value a random set of initial data is chosen and the orbits from each such point are calculated over several hundreds periods (or iterations in case of IM) without storing, than the flow is continued for another hundred periods and stored. To see how the set changes, a small parameter adjustment is made and the process is repeated. The solution data obtained in such a manner can then be plotted in a bifurcation diagram where solutions are plotted against the parameter. Plots obtained in this way via direct numerical simulations are sometimes referred as Monte Carlo (or brute-force) bifurcation diagrams. This method has the advantage of capturing most (but not all) of the long time dynamics, but has the disadvantage of not being able to capture unstable behavior, which requires another algorithm, called *path-following*.

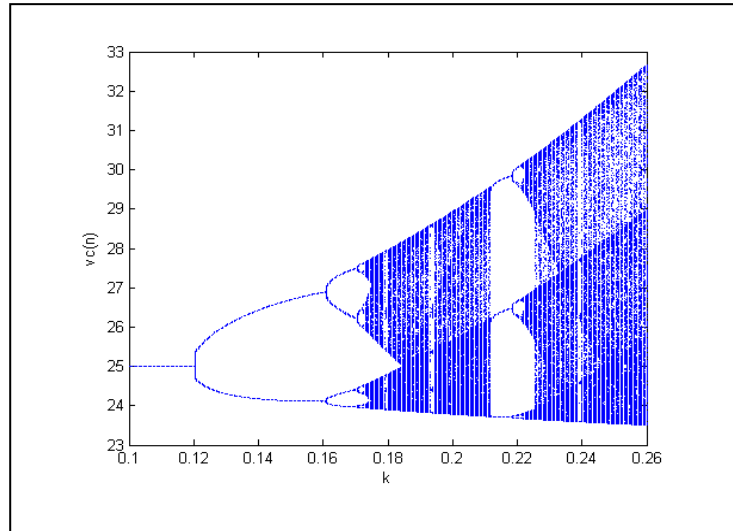


Fig.2. Monte Carlo bifurcation diagram for buck in DCM mode

In order to plot the brute-force bifurcation diagram it is necessary to calculate the Poincare section for the systems under investigation. In this case the most suitable would be the use of IM. It appears that it is not possible to obtain the closed form IM for the buck converter operating in CCM, as finding the switching instants in this case involves solving of transcendental equations. However, the operation of buck converter in DCM (when v_C is the only state variable) was studied by Tse [7], who obtained discrete time one dimensional map:

$$v_{C,n+1} = \frac{\alpha v_{C,n}^2 + E(E - v_{C,n})\beta(H(D - k(v_{C,n} - V_{ref})))^2}{v_{C,n}}, \quad (5)$$

$$D = \sqrt{\frac{(1-\alpha)V_C^2}{\beta E(E - V_C)}}; \alpha = 1 - \frac{T}{RC} + \frac{T^2}{2C^2 R^2}; \beta = \frac{T^2}{2LC}; H(x) = \begin{cases} 0, & \text{if } -x < 0 \\ 1, & \text{if } -x > 1 \\ x, & \text{otherwise.} \end{cases}$$

Where T is the switching period, V_C – steady–state output voltage and all other parameters are depicted on the Fig.1. The example of Monte Carlo bifurcation diagram obtained on the basis of this map is shown in the Fig.2. The parameters used are as follows: T=333.3(μs); R=12.5(Ω); L=208(μH); C=222(μF); V_{ref} =25(V); E=33(V). Small signal feedback gain was

chosen as the bifurcation parameter. This diagram clearly shows that the period-doubling route to chaos is initiated as k reaches 0.12.

While having the merits described above, direct simulation suffers from two main disadvantages – it does not accurately pinpoint bifurcation points and computes only stable periodic orbits. In order to accurately locate bifurcations it is sometimes necessary to compute unstable orbits. These typically comprise methods for numerical path-following of the solutions as parameter varies, for detecting codimension-one bifurcations, and possibly continuation of these bifurcation points in two or more parameters.

The key idea behind numerical continuation is to compute sequences of points at small intervals along the solutions curve [8, 9]. The most commonly used method to compute sequences of fixed points is Newton-Raphson method, but it is known that this requires a sufficiently good initial guess in order to converge. There exists a variety of modifications of this method, but the strength of approach is the ability to follow unstable periodic orbits and to detect regimes skipped in the process of construction of Monte Carlo bifurcation diagrams. It is important to note, that when computing solutions to piecewise – smooth systems, it is usually not possible to use general purpose nonlinear software packages (such as AUTO [10], MATHCONT[11] etc.), as most numerical integration routines assume high degree of smoothness of the solution. Accurate computations must take special allowance for the non-smooth events that occur when switching boundary is crossed. Hence, using the conditions given above, special numerical continuation routines were written in MATLAB to trace the bifurcation curves in one parameter space.

Fig.3. shows the example bifurcation diagram of the buck converter operating in DCM, computed on the basis of IM (5) by means of path-following approach.

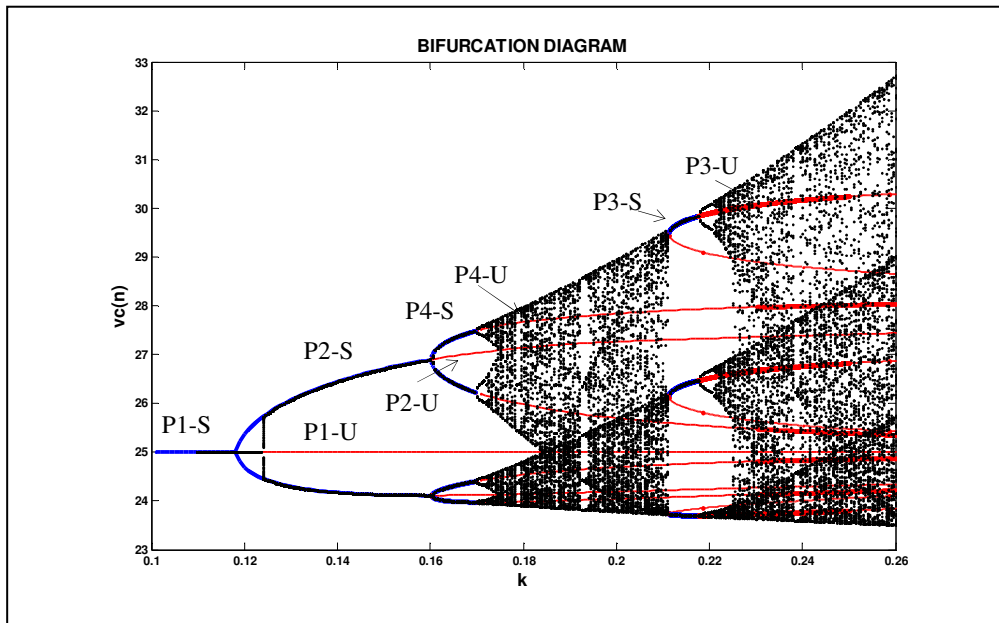


Fig.3. Bifurcation diagram for buck in DCM mode

For the sake of simplicity, this diagram depicts bifurcation branches of just few operating regimes. Blue lines represent stable orbits, thick red lines represent unstable periodic orbits (period-1 orbit is marked as P1, period-2 as P2 etc.), and black dots illustrate the brute-force diagram. It could be seen, that numerical continuation approach allows the detection of all possible periodic windows between chaotic bands as well it gives a possibility to explain some nonlinear phenomena. For example, the sudden birth of wide band chaos from several chaotic bands at $k=0.223$ could be explained by collision of chaotic attractor with unstable

period-3 orbit. This could be clearly seen on Fig.3., however, Monte Carlo bifurcation diagram does not show the explanation of this expansion of chaotic attractor.

As it was already mentioned, the path-following algorithm also uses the Poincare section of the flow, but authors established, that in the case of path-following approach, there exist some special requirements to the “quality” of this section.

The Fig.4.a. represents the phase portrait obtained for the period-1 operation of the buck converter, solving equations (1)-(4) by means of Runge – Kutta method. The solid blue line shows the exact solution of equations, and the black dashed lines – numerical solution. If this phase portrait is used to construct the Poincare section (Fig.4.b.), instead of one equilibrium point (big blue cross), corresponding to the period-1 operation, it will show a variety of points (small black crosses) around it.

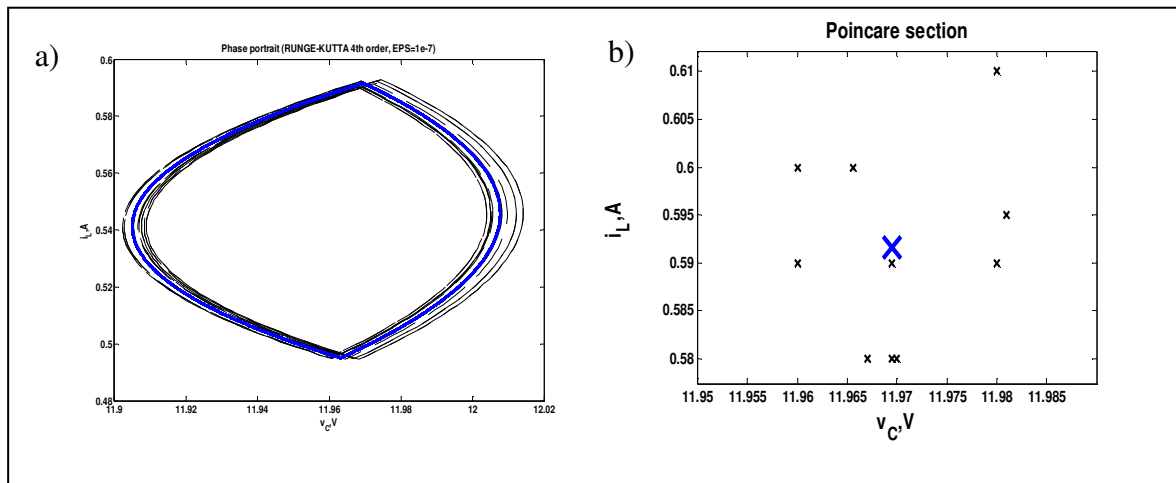


Fig.4. Period-1 phase portrait and Poincare section

Constructing the brute-force bifurcation diagram using this kind of data will lead to slightly “spread” but still sufficiently clear diagrams, similar to those, obtained in laboratory experiments in the presence of unavoidable noise. On the other hand, using the path-following algorithm, the Newton-Raphson or similar methods would not converge or will erroneously detect period-n ($n > 1$) operation regime. So in the case of numerical continuation some special modifications of the model should be used.

The main cause of the “spread” phase portrait is that the use of numerical integration with fixed step does not allow precise detection of switching events, which leads to the fact, that even in the period-1 regime at the beginning of every cycle different starting points for the next calculations are used. We propose some useful methods, used by author in order to overcome this problem. The most straightforward approach is to decrease the overall integration step until the error becomes negligible. As the main drawback of the mentioned method is the unacceptable increase of simulation time and number of data points, the author would suggest using some numerical integration algorithms with variable step size in order to decrease the step only in the vicinity of switching event. Thus, appropriately choosing integration steps, it is possible to obtain clear Poincare sections and apply path following approach.

Results and discussion

Various types of models of the buck converter operating in CCM and DCM have been studied in order to detect their suitability and reliability in numerical investigation of nonlinear phenomena. The comparison of models is presented in the Table. As it was already

mentioned, all models could be used in order to detect subharmonics and chaos in DC-DC converters, however it is extremely important to acknowledge the possibilities and limitations of the use of these nonlinear models.

Table

The comparison of models

Model	Comments
SSSM (analytically solved)	+ Fast and precise models; -Could be applied in case of relatively simple systems of linear differential equations.
SSSM (numerical integration)	+No need to solve equations by hand; -The slowest models; - In order to use for numerical path following requires some special particularization measures.
Iterative mapping	+ Direct use in brute-force approach and path following; +Simple and fast models; - Impossible to use for buck converter operating in CCM; - Requires simplification procedures – some useful information could be lost.

It could be seen from the Table that the most attractive from the practical point of few is the IM, however, it has several noticeable drawbacks, that limit the applicability of this kind of models. In order to investigate the dynamics of buck converter, operating in CCM, we still have to use the SSSM. The example given in this paper and illustrated in the Fig.3. shows, that the use of numerical path following technique gives the most full understanding of nonlinear behavior of dynamical systems and allows explaining some complex phenomena. So the next step in this kind of research would be the development of systematic procedure, capable of merging the SSSM models of DC-DC converters and numerical path-following techniques in order to create the complete bifurcation diagrams, indicating all possible operating regimes.

Conclusions

This paper is concerned with the problem of modeling and simulation of piecewise linear systems with PWM control. The research provides the study of buck converter under voltage mode control, operating in DCM and CCM, using Matlab and Simulink simulation environment. Various types of models, including IM and SSSM, are examined in order to detect their suitability and reliability in numerical investigation of nonlinear phenomena in DC–DC converters. The main advantages and disadvantages of different models are pinpointed and some recommendations for the most appropriate use and implementations are provided. It has been shown that direct simulation does not accurately locate bifurcation points and computes only stable invariant set, so some aspects and advantages of numerical path-following are also discussed. The complete bifurcation diagrams, obtained by means of numerical continuation could be used not only to predict the operating regimes in given parameter range, but could also provide useful information for different nonlinear control schemes, when it is necessary to find and stabilize/destabilize certain periodic orbits (this technique is widely used in chaotic control [12] and spread spectrum applications[13,14]).

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